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CONFIRMATION NO. 4723

SERIAL NUMBI 10/787,436	ER	FILING DATE 02/27/2004 RULE		LASS 438	GRO	JP ART 2813	UNIT	D(ATTORNEY OCKET NO. 0.38899VX3
APPLICANTS			•						
Naoki Yamamoto, Kawaguchi, JAPAN;									
** CONTINUING DATA **********************************									
IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 05/15/2004									
Foreign Priority claimed 35 USC 119 (a-d) cond met		yes no Met after	Allowance	STATE OR		EETS		TAL	INDEPENDENT
Verified and Acknowledged	Exa	aminer's Signature II	nitials	COUNTRY JAPAN	DRA	AWING 7	5	NMS 5	CLAIMS 3
ADDRESS 020457 ANTONELLI, TERRY, STOUT & KRAUS, LLP 1300 NORTH SEVENTEENTH STREET SUITE 1800 ARLINGTON , VA 22209-9889									
TITLE Method of manufacturing a dual gate semiconductor device with a poly-metal electrode									
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	No.	to charge/cre	Authority has been given in Par		NΤ	☐ 1.16 Fees (Filing) ☐ 1.17 Fees (Processing Ext. of time)			
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